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### [J Crawford](#)

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AP Bradley, D Yip, AJ Dorrell - US Patent 7,151,863, 2006 - freepatentsonline.com  
 ... embodiment, a boxcar filter is applied to a **block** of k ... 1405 control the operation of a 3-to-1 **multiplexer** 1407, as ... For 4:2:0 YUV **input** pixels, the size of the ...  
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JA Crawford - 2003 - freepatentsonline.com  
 ... for each data symbol are coupled to **multiplexer** 404. ... 9, a functional **block** diagram is shown of the pilot ... delay from the arrival of the last **input** sample pair (l ...  
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JA Crawford - US Patent 6,549,561, 2003 - Google Patents  
 ... rotator rotates the incoming signal by the filtered phase error estimate for subsequent OFDM symbols to reduce the phase noise of the signaling **output** from the ...  
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L Kwiatkowski, F Boéri, JP Stromboni - ee.ust.hk  
 ... techniques proposed for the identification of state space models have focused on using the singular value decomposition of **block** Hankel **input-output** matrices. ...  
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[Tunable, low back-reflection wavelength division multiplexer](#) - all 3 versions

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WZ Li, F Liu, H Lu - US Patent 6,084,994, 2000 - Google Patents

... and system for providing a wavelength division **multiplexer**. ... 2 depicting a **block** diagram

of one ... around a different wavelength for each of the **composite signals**. ...

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[Macrocell with product-term cascade and improved flip flop utilization](#) - all 4 versions »

D Chiang, NW Lee, TY Ho, DA Harrison, N ... - US Patent 5,357,153, 1994 - Google Patents

... FIG. 1 is a **block** diagram of prior art PLD architecture - 5 logical equivalent such as a 2-input AND gate, etc. ... 4. In the **multiplexer** 76 **composite signals** CB-CKM ...

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GCP Lokhoff, YF Dehery, GJ Stoll, G Theile - US Patent 5,539,829, 1996 - Google Patents

... Lokhoff et al. US005539829A [11] Patent Number: [45] Date of Patent: [54] SUBBAND CODED DIGITAL TRANSMISSION SYSTEM USING SOME **COMPOSITE SIGNALS** ...

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TC Butash - US Patent 6,351,451, 2002 - Google Patents

... 7 of a **multiplexer** in accordance with the ... an exemplary embodiment of a functional **block** diagram of ... The real base-50 banded **composite signals** are inputted to ...

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[Multi-code compressed mode DS-CDMA systems and methods](#) - all 3 versions »

E Dahlman, PHP Willars, OE Grimlund, LM Ewerbring - US Patent 5,896,368, 1999 - Google Patents

... a number K of so interleaved frame in time **multiplexer** 260 ... 2C, the **composite signals** from ... scrambling (at **blocks** 300 and 302) the composite signal 45 directed to ...

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[Code-rate increased compressed mode DS-CDMA systems and methods](#) - all 3 versions »

E Dahlman, PHP Willars, OE Grimlund, LM Ewerbring - US Patent 5,883,899, 1999 - Google Patents

... encoded frame is subjected to **block**-wise bit ... encoded/interleaved frame in time **multiplexer** 260 ... 2C, the **composite signals** from each downlink traffic information ...

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TE Dwan, TE Bechert, USN Acad, MD Annapolis - Frontiers in Education Conference, 1993. Twenty-Third Annual ..., 1993 - [ieeexplore.ieee.org](#)

... 1 to create a new scope, automatically named Scope 2. G. To interconnect the **blocks**:  
 1. Connect ... 2. Connect **output** port of Signal Gen to **input** port of Mux. ...

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[Satellite Systems Design/Simulation Environment: A Systems Approach to Pre-Phase A Design - all 10 versions »](#)

MJ Ferebee Jr, PA Troutman, DW Monell - AIAA, Aerospace Sciences Meeting & Exhibit, 35th, Reno, NV, 1997 - [pdf.aiaa.org](#)

... characteristics of system components with parameter **input** menus. ... Lines connecting the **blocks** represent the flow of ... time plots of any block's **input** or **output** values. ...

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[On-line control of a real system with MATLAB/SIMULINK](#)

A Davari, D Shen - System Theory, 1998. Proceedings of the Thirtieth ..., 1998 - [ieeexplore.ieee.org](#)

... Page 3 On Toole2 ODOD ToWje1 Tod 9 **block** is used for **output** monitoring (one can monitor **input** on the same scope by adding a "MUX" **block** to the ...

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DK Hiranaka, G Student, ET Vinande, U Student, DC ... - [pdf.aiaa.org](#)

... **block** is available in the Simulink library of nonlinear **blocks**. ... Passing the proper **input** array is accomplished with a Simulink **multiplexer**. ...

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A COST, R TIME, FSC GENERATION IN AN - [aerosim.calpoly.edu](#)

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[Asymptotic zero-transition activity encoding for address busses in low-power microprocessor-based ... - all 5 versions »](#)

L Benini, G De Micheli, E Macii, D Sciuto, C ... - VLSI, 1997. Proceedings. Seventh Great Lakes Symposium on, 1997 - [ieeexplore.ieee.org](#)

... of a large set of properly selected **input** patterns when ... of transitions that stream of addresses is **output** on the bus ... scheme is essential, since in cache **block** is a ...

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[\[PS\] The McGill Sarcos Robots: System Architecture and Programming Environment - all 3 versions »](#)

M Doyon, LJ CIM - [cim.mcgill.ca](#)

... Mux ... background tasks (eg inputX.[ch], outputX.[ch], traceX.[ch], in the **Matlab** environment ... as dummy since they are only used to define the **block input-output** size ...

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**The Development of Vehicular Powertrain System Modeling Methodologies: Philosophy and Implementation - all 2 versions »**

ZJ Rubin, SA Munns, JJ Moskwa - 1997 SAE Congress and Exposition, Detroit, SAE paper - simcar.com

... data sets and some **input-output** structure ... of detail inside the engine **block** module reveals ... state\_cylinders flow\_intake\_manifold\_out Mux Mux1 4 flow\_turb\_in Mux ...

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**Application of the MATLAB-Simulink package in the simulation tests on hydrostatic systems**

Z Jędrzykiewicz, J Pluta, J Stojek - actamont.tuke.sk

... 17 presents a general **block** diagram, made after having aggregated ... It illustrates the data loading, **input** signals and taking ... Hydraulic motor M h Load Mux 2 ω h ...

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**A novel current controller without overshoot for PWM converters with the varying control bounds**

JW Lee - Industrial Electronics, Control, and Instrumentation, 1996., ..., 1996 - ieeexplore.ieee.org

... L+Kp/L) Page 3 (b) Tracking anti-windup controller Clock Mux Freq 135 ... 3. SIMULINK **block diagrams** of feedback controllers **output** follows the **input**. ...

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